

The listed items were cited by the Japanese Patent Office in a counterpart application, namely Appln. No. 2004-081699. A copy of the Japanese Office Action dated September 5, 2006 is attached hereto.

The filing of this Supplemental Information Disclosure Statement is not an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

The claims of the application as now presented are believed to patentably distinguish over the prior art and to be in condition for allowance. Early and favorable consideration of the case is respectfully requested.

CERTIFICATION UNDER 37 C.F.R. §1.97(e)(2)

Applicant submits that each item of information contained in the Supplemental Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application no more than three months prior to the filing of the Statement.

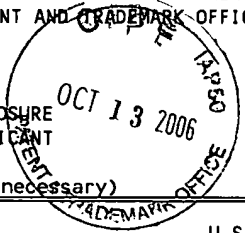
Respectfully submitted,



Paul J. Farrell
Reg. No. 33,494
Attorney for Applicant

DILWORTH & BARRESE, LLP
333 Earle Ovington Blvd.
Uniondale, NY 11553
(516) 228-8484
(516) 228-8516

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		APPLICANT Jai-Ho LEE	
		FILING DATE March 10, 2004	GROUP ART UNIT 2664



U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	WO 02/07388	1/24/2002	PCT			X	
	JP 2003-032266	1/31/2003	Japan			X	
	WO 02/103943	12/27/2002	PCT			X	
	JP 2003-23434	1/24/2003	Japan			X	

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

		1.	Nikkei Electronics, Ethernet, May 6, 2002, pp. 100-107.

EXAMINER	DATE CONSIDERED
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* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.